

Amendments to the Claims:

A clean version of the entire set of pending claims, including amendments to the claims, is submitted herewith per 37 CFR 1.121(c)(3). This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently Amended) A packet formatter (240)-for use in a television receiver (200)-capable of receiving a dual bitstream signal comprising a standard stream compatible with the Advanced Television Systems Committee (ATSC) standard and a robust stream, said packet formatter (240)-comprising:

a first processing block (410)-capable of receiving said a dual bitstream signal comprising a standard stream compatible with the Advanced Television Systems Committee (ATSC) standard and a robust stream, the robust stream having associated therewith and removing therefrom header bits bytes and parity bits bytes, locations of the parity bytes being dependent upon a position of a current packet within a frame of packets in the dual bitstream signal associated with said robust stream to thereby produce a first output signal; [[and]]

a second processing block (430)-capable of determining the locations of the parity bytes within the current packet according to the current packet's position within its frame, in response to which the first processing block removes the header bytes and parity bytes from dual bitstream signal to output a first output signal; and

a third processing block capable of receiving said first output signal and removing therefrom duplicate bits associated with said robust stream to thereby produce a second output signal that is output from a data path output (295)-of said packet formatter.

2. (Currently Amended) The packet formatter (240)-as set forth in claim 1 wherein said packet formatter (240)-passes bytes associated with said standard stream to said data path output (295)-of said packet formatter (240)-after delaying

said standard stream bytes by a predetermined delay time.

3. (Canceled)

4. (Currently Amended) The packet formatter (240)-as set forth in claim [[3]]11 wherein said third second processing block (420)-is further capable of determining the locations of said header bits-bytes in said robust stream.

5. (Currently Amended) The packet formatter (240)-as set forth in claim [[4]]11 wherein said third second processing block (420)-comprises includes a look-up table which identifies the locations of the parity bytes for each packet depending upon the location of the packet within the frame (420).

6. (Currently Amended) The packet formatter (240)-as set forth in claim 5 wherein said packet formatter (240)-generates and outputs packet identification information used by subsequent processing blocks (250, 260, 270)-following said packet formatter-(240).

7. (Currently Amended) A signal comprising the second output signal output from the data path output of the packet formatter-(240) as set forth in claim 1.

8.-(Currently Amended) For use in a television receiver (200)-capable of receiving a dual bitstream signal comprising a standard stream compatible with the Advanced Television Systems Committee (ATSC) standard and a robust stream,the robust stream having associated therewith header bytes and parity bytes, locations of the parity bytes being dependent upon a position of a packet within a frame of packets in the dual bitstream signal, a method of formatting packets of said dual bitstream signal comprising the steps of:

receiving in a packet formatter (240)-said dual bitstream signal;

determining the locations of the parity bytes within a current packet according

to the current packet's position within its frame; and

removing therefrom the header bits-bytes and parity bits-bytes associated with said robust stream from the dual bitstream signal to thereby produce a first output signal; and

removing from said first output signal duplicate bits associated with said robust stream to thereby produce a second output signal that is output from a data path output (295) of said packet formatter-(240).

9. (Currently Amended) The method as set forth in claim 8 further comprising the step of delaying bytes associated with said standard stream by a predetermined delay time before outputting said delayed standard stream bytes on said data path output-(295) of said packet formatter-(240).

10. (Canceled)

11. (Currently Amended) The method as set forth in claim [[10]]8 further comprising the step of determining the locations of header bits-bytes in said robust stream.

12. (Currently Amended) The method as set forth in claim [[11]]8 wherein said step of determining the locations of said parity bits comprises the step of determining the locations of said parity bits from a look-up table-(420).

13. (Currently Amended) The method as set forth in claim 12 further comprising the steps of generating and outputting packet identification information used by subsequent processing blocks (250, 260, 270) following said packet formatter-(240).

14. (Currently Amended) A signal comprising the second output signal output from the data path output of the packet formatter-(240) as set forth in claim 8.

15. (Currently Amended) A television receiver (200) comprising:
receiver front-end circuitry capable of receiving and down-converting a dual bitstream signal comprising a standard stream compatible with the Advanced Television Systems Committee (ATSC) standard and a robust stream having associated therewith and removing therefrom header bits bytes and parity bits associated with said robust stream, to thereby produce bytes, locations of the parity bytes being dependent upon a position of a current packet within a frame of packets in the dual bitstream signal, the receiver front-end circuitry producing a baseband signal; and

a forward error correction section capable of receiving said baseband signal from said receiver front-end circuitry, said forward error correction section comprising a packet formatter (240)-comprising:

a first processing block (410)-capable of receiving said standard stream and said robust stream associated with said baseband signal and removing therefrom header bits and parity bits associated with said robust stream to thereby produce a first output signal;

a second processing block capable of determining the locations of the parity bytes within the current packet according to the current packet's position within its frame, in response to which the first processing block removes from the header bytes and parity bytes from dual bitstream signal to output a first output signal; and

a second third processing block (430) capable of receiving said first output signal and removing therefrom duplicate bits associated with said robust stream to thereby produce a second output signal that is output from a data path output (295) of said packet formatter (240);

a robust deinterleaver capable of receiving the second output signal, and deinterleaving data in the robust stream to output a third output signal;

a Reed-Solomon decider capable of receiving the third output signal, and decoding data in the third output signal to output a fourth output signal; and

a derandomizer capable of receiving the fourth output signal and

derandomizing bytes associated with said standard stream and bytes associated with said robust stream.

16. (Currently Amended) The television receiver-(200) as set forth in claim 15 wherein said packet formatter-(240) passes bytes associated with said standard stream to said data path output-(295) of said packet formatter-(240) after delaying said standard stream bytes by a predetermined delay time.

17. (Canceled)

18. (Currently Amended) The television receiver-(200) as set forth in claim [[17]]15 wherein said third-second processing block-(420) is further capable of determining the locations of said header bits-bytes in said robust stream.

19. (Currently Amended) The television receiver-(200) as set forth in claim 18 wherein said third-second processing block-(420) comprises a look-up table-(420).

20. (Currently Amended) The television receiver-(200) as set forth in claim 19 wherein said packet formatter-(240) generates and outputs packet identification information used by subsequent processing blocks (250, 260, 270)-following said packet formatter-(240).

21. (Canceled)

22. (Currently Amended) The data de-randomizer (270) as set forth in claim 21 A data de-randomizer for use in a television receiver capable of receiving a dual bitstream signal comprising a standard stream compatible with the Advanced Television Systems Committee (ATSC) standard and a robust stream, said data de-randomizer comprising:

a standard de-randomizer capable of de-randomizing bytes associated with

said standard stream; and

a robust de-randomizer capable of de-randomizing bytes associated with said robust stream

wherein said data de-randomizer (270)-further comprises a delay calculation circuit (740,750)-for determining a delay with respect to a field synchronization signal associated with the robust stream.